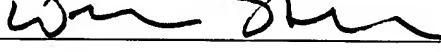




Docket No.: MUH-12728

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By: 

Date: April 11, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/631,350 Confirmation No: 1926
Applicant : Hans Weber et al.
Filed : July 31, 2003
Title : Method for Forming a Channel Zone of a Transistor and PMOS Transistor
Art Unit : 2818
Examiner : Dao H. Nguyen

Docket No. : MUH-12728
Customer No. : 24131

R E S P O N S E

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated March 10, 2005, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 4-6 and 11-12, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 4-6 and 11-12 is solicited.

Respectfully submitted,



For Applicants

WERNER H. STEMER
REG. NO. 34,956

Date: April 11, 2005

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